

REMARKS

This responds to the Office Action mailed on October 16, 2006. Claims 1, 7, 13, 18 and 25 are amended, claims 9, 11, 16, 20 and 26 are canceled; as a result, claims 1-8, 10, 12-15, 17-19, 21-25 and 27-30 are now pending in this application. Applicant does not admit that the cited references are prior art and reserves the right to swear behind such references at a later date.

§102 Rejection of the Claims

Claims 1 and 2 were rejected under 35 USC § 102(b) as being anticipated by Tulpule et al. (U.S. 4,933,836). Applicant respectfully traverses the rejection.

Anticipation requires the disclosure in a single prior art reference of each element of the claim under consideration.¹ It is not enough, however, that the prior art reference discloses all the claimed elements in isolation. Rather, “[a]nticipation requires the presence in a single prior reference disclosure of each and every element of the claimed invention, *arranged as in the claim*.”²

As amended, claim 1 recites “wherein the two or more I/O ports in the first processor, the second processor and the third processor are configured to establish a logical connection between the first processor and the second processor, the logical connection to originate at first processor and to traverse through the third processor and to complete at the second processor, wherein the logical connection is established based on other active logical connections that include at least one of the first processor, the second processor and third processor.” (emphasis added). Applicant respectfully submits that cited art discloses such limitation.

First, the Office seems to equate the “datapipe” of Galicki with a logical connection (see Office Action at page 3). Applicant respectfully traverses this assertion. The datapipe of Galicki is referencing the three internal components within a processor:

¹ *In re Dillon* 919 F.2d 688, 16 USPQ 2d 1897, 1908 (Fed. Cir. 1990) (en banc), cert. denied, 500 U.S. 904 (1991).

² *Lindemann Maschinenfabrik GmbH v. American Hoist & Derrick Co.*, 730 F.2d 1452, 221 USPQ 481, 485 (Fed. Cir. 1984) (citing *Connell v. Sears, Roebuck & Co.*, 722 F.2d 1542, 220 USPQ 193 (Fed. Cir. 1983)) (emphasis added).

A datapipe routing bridge peripheral is composed of three building blocks, a transmitter, a bridge and a receiver.
Galicki at col. 1, lines 30-31.

The datapipe of Galicki, thus, relates to the components for processing a packet within a processor. The datapipe of Galicki does not relate to a logical connection between the processors. Moreover, Applicant respectfully submits that the cited art does not disclose the establishing of a logical connection based on other active logical connections (as recited).

Accordingly, the cited art does not disclose all of the claim limitations of claim 1. Applicant respectfully submits that the rejection of claim 1 under 35 U.S.C. §102 has been overcome. Claim 2 depends from claim 1 and distinguishes the reference for at least the same reason.

§103 Rejection of the Claims

Claims 3-12 and 18-30 were rejected under 35 USC § 103(a) as being unpatentable over Tulpule et al. (U.S. 4,933,836) in view of Galicki et al. (U.S. 6,967,950). Applicant respectfully traverses the rejection. Claims 13-17 were rejected under 35 USC § 103(a) as being unpatentable over Tulpule et al. (U.S. 4,933,836) in view of Galicki et al. (U.S. 6,967,950), and further in view of Hsieh et al. (U.S. 6,757,019).

In order for the Examiner to establish a *prima facie* case of obviousness, three base criteria must be met. First, there must be some suggestion or motivation, either in the references themselves or in the knowledge generally available to one of ordinary skill in the art, to modify the reference or to combine reference teachings. Second, there must be a reasonable expectation of success. Finally, the prior art reference (or references when combined) must teach or suggest all the claim limitations. The teaching or suggestion to make the claimed combination and the reasonable expectation of success must both be found in the prior art, and not based on applicant's disclosure.³

As amended, claim 7 recites "wherein the source image signal processor is to transmit an initialize signal, prior to transmission of data along the logical connection, through the number of intermediate image signal processors to the destination image signal processor in

³ *M.P.E.P.* § 2142 (citing *In re Vaeck*, 947 F.2d 488, 20 USPQ2d 1438 (Fed.Cir. 1991)).

the order that data is transmitted in the logical connection.” In particular, Applicant has amended claim 7 to incorporate the limitations of claim 11.

With regard to the rejection of claim 11, the Office indicated that Galicki et al. (U.S. 6,967,950) at Fig. 5, col. 5, ll. 48-62 discloses this limitation. Applicant respectfully traverses this assertion. This section of Galicki relates to transmitting of a packet with a header (for routing) being transmitted over the “datapipe network” from a “source digital signal processor” to the “destination.” Thus, this section of Galicki illustrates a data packet being transmitted based on a header of the packet. This section of Galicki does not disclose or suggest the transmission of an initialize signal prior to data transmission along the logical connection.

Therefore, neither reference alone or in combination, disclose or suggest all of the claim limitations. Accordingly, Applicants respectfully submit that the rejection of claim 7 under 35 U.S.C. §103 has been overcome. Claims 8, 10 and 12 depend from claim 7 and distinguish the references for at least the same reason.

With regard to claims 13-15, 17-19, 21-25 and 27-30, Applicant respectfully submits that the Office Action did not make out a *prima facie* case of obviousness because even if combined, the cited references fail to teach or suggest all of the elements of claims 13-15, 17-19, 21-25 and 27-30. In particular, for at least the reasons set forth above regarding claims 1 and 7, Applicant respectfully submits that the cited references fail to teach or suggest all of the elements of claims 13-15, 17-19, 21-25 and 27-30. Accordingly, Applicants respectfully submit that the rejection of claim 13-15, 17-19, 21-25 and 27-30 under 35 U.S.C. §103 has been overcome.

Conclusion

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney ((612) 371-2103) to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,


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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 16th day of April 2007.

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